**In the Claims:**

Please amend claims 1-8 as indicated below. This listing of claims replaces all prior versions.

1. (Currently amended) A resistor network ~~(2)~~ such as a resistor ladder network, comprising at least a resistor body ~~(4)~~ that includes multiple resistor sub-bodies which ~~is~~ are each ~~provided with~~ connected to and ~~at least a column (6) of taps (8)~~ situated between ~~a first~~ columns of taps ~~and a second tap~~, wherein, in use, at least two taps can be connected with respective first and second sources of reference input potentials, and wherein each tap of the ~~at least one~~ columns of taps can be used for outputting an output potential via a contact area which is connected with the concerning tap, ~~characterised in that, the resistor body (4) comprises a multiple of resistor sub-bodies (5), wherein each resistor sub-body is connected with a column (6) of taps (8),~~ and wherein the only electrical connections between the resistor sub-bodies ~~(5)~~ are electrical connections via the taps ~~(8)~~ connected with the resistor sub-bodies ~~(5)~~.

2. (Currently amended) A resistor network ~~(2)~~ according to claim 1, ~~characterised in that, each resistor sub-body (5) is provided with at least one column (6) of taps (8),~~ wherein each tap ~~(8)~~ of the columns ~~(6)~~ of taps ~~(8)~~ is an extremity such as a T-shaped or S-shaped projection which is connected with one of the ~~concerning~~ resistor sub-bodies ~~sub-body ( 5 )~~.

3. (Currently amended) A resistor network ~~(2)~~ according to claim 1, characterised in that, a plurality of taps ~~(8)~~ of a first column ~~(6)~~ of taps ~~(8)~~ of a first resistor sub-body is connected with a plurality of taps ~~(8)~~ of a second column of taps of a second resistor sub-body, wherein each tap ~~(8)~~ of the plurality of taps ~~(8)~~ of the first column is connected with only one tap of the plurality of taps of the second column and wherein each tap ~~(8)~~ of the plurality of taps ~~(8)~~ of the second column is connected with only one tap of the plurality of taps of the first column.

4. (Currently amended) A resistor network according to claim 3, characterised in that, each tap ~~(8)~~ of the first column is shifted at least one column position with respect to the column position of the tap of the second column with which the tap of the first column is connected.

5. (Currently amended) A resistor network according to claim 3, characterised in that, the respective connections between the resistor sub-bodies with the taps ~~(8)~~ are made during fabrication of the resistor network at different arbitrary positions with respect to the respective taps

6. (Currently amended) A resistor network ~~(2)~~ according to claim 1, characterised in that, the resistor network ~~(2)~~ is a semiconductor circuit, wherein each resistor sub-body comprises a number of resistor layers, wherein each semi-conducting resistor layer comprises at least two taps, and wherein the semi-conducting resistor layers are interconnected via the taps.

7. (Currently amended) An Analog-Digital converter for generating a digital output signal on the basis of an analog input signal, characterised in that, the Analog-Digital converter comprises a resistor network ~~(2)~~ according to claim 1.

8. (Currently amended) A Digital-Analog converter for generating an analog output signal on the basis of a digital input signal, characterised in that, the Digital-Analog converter comprises a resistor network ~~(2)~~ according to claim 1.

9. (Cancelled)

10. (Cancelled)

11. (Cancelled)